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OFFICE OF PETITIONS

Applicant: Ahn et al.
Appl. No.: 10/696,615
Filing Date: October 28, 2003
Title: SEMICONDUCTOR MEMORY DEVICES HAVING OFFSET TRANSISTORS AND
METHODS OF FABRICATING THE SAME
Attorney Docket No.: 5649-1202
Pub. No.: US 2004/0084698 A1
Pub. Date: May 6, 2004

This is a decision on the request for corrected patent application publication under
37 CFR 1.221(b), filed on May 27, 2004, for the above-identified application.

The request is granted.

Inquiries relating to this matter may be directed to Mark Polutta at (703) 308-8122 (voice).

Mark O. Polutta
Office of Patent Legal Administration
Office of the Deputy Commissioner
for Patent Examination Policy